Problem Set # 10/11

2) Transfer Operation Control Signals

R[0] ← R[0] READR0, XFER, WRITER0
R[0] ← R[1] READR1, XFER, WRITER0
R[0] ← R[2] READR2, XFER, WRITER0
R[0] ← R[3] READR3, XFER, WRITER0
R[1] ← R[0] READR0, XFER, WRITER1
R[1] ← R[1] READR1, XFER, WRITER1
R[2] ← R[0] READR0, XFER, WRITER2
R[3] ← R[0] READR0, XFER, WRITER3