HW 5: Solutions for Problems 1 and 2

1a) $S' \quad Q \quad R' \quad Q'$

1b) $R' \quad S' \quad Q \quad Q'$

reset hold set

reset hold set

forbidden

1c) $Q, Q', Q'' \quad Q, Q', Q''$

1d) $Q(t') = S + R'Q(t)$

1e) $S \quad \quad \quad \quad Q \quad \quad \quad \quad Q'$

enable

R
2b) This implementation does not suffer from the ones catching problem. $S'$ and $R'$ being both being one does not cause a problem.

2c) This implementation does, however, suffer from the zeroes catching problem. $S'$ and $R'$ both being 0 causes the output to be unstable.